

PRIOR ART

FIGURE 1

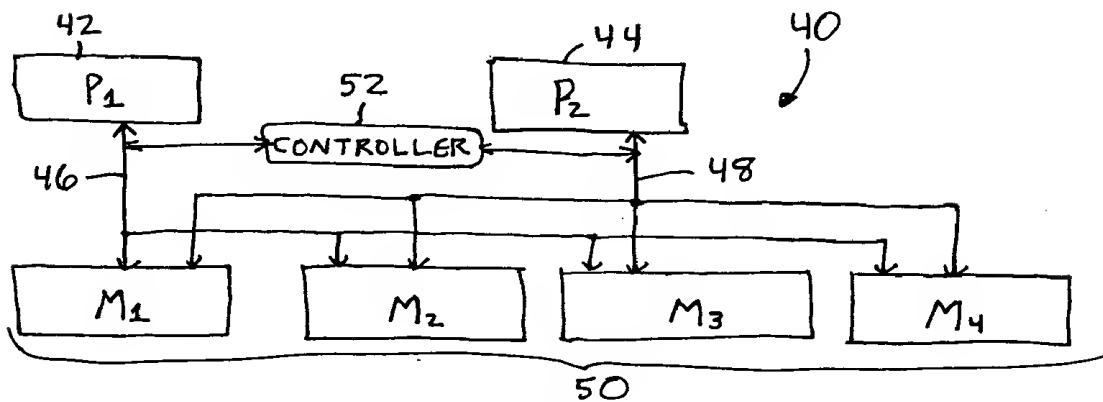


FIGURE 2

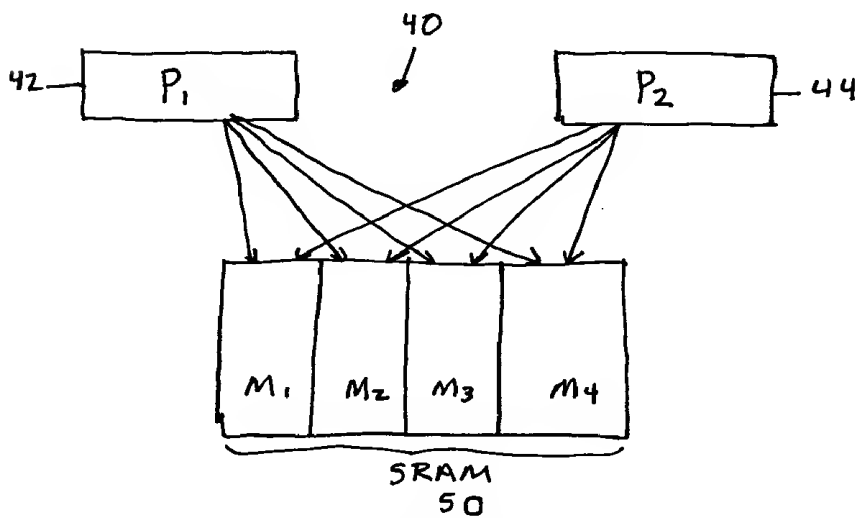


FIGURE 3

09847991.050201
F02050.T6674860

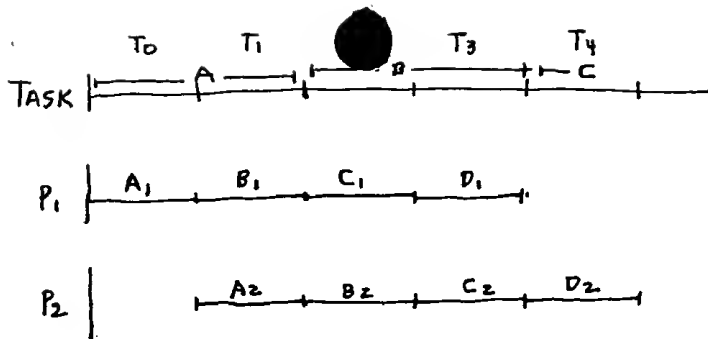


FIGURE 4

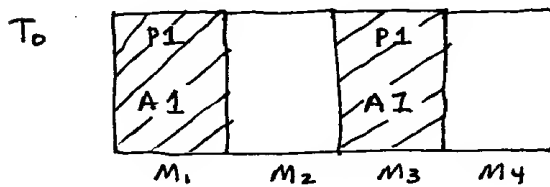


Figure 5A

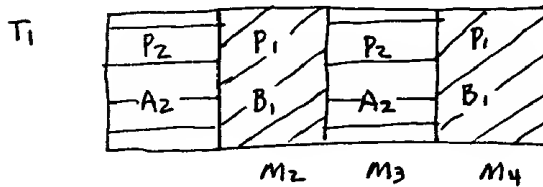


Figure 5B

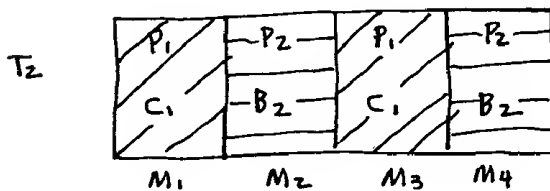


Figure 5C

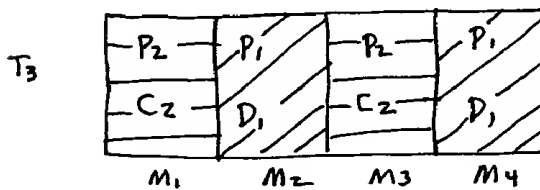


Figure 5D

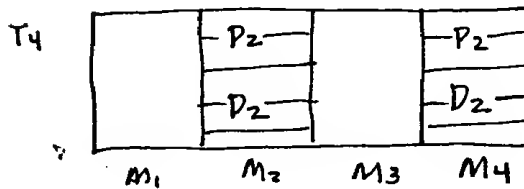
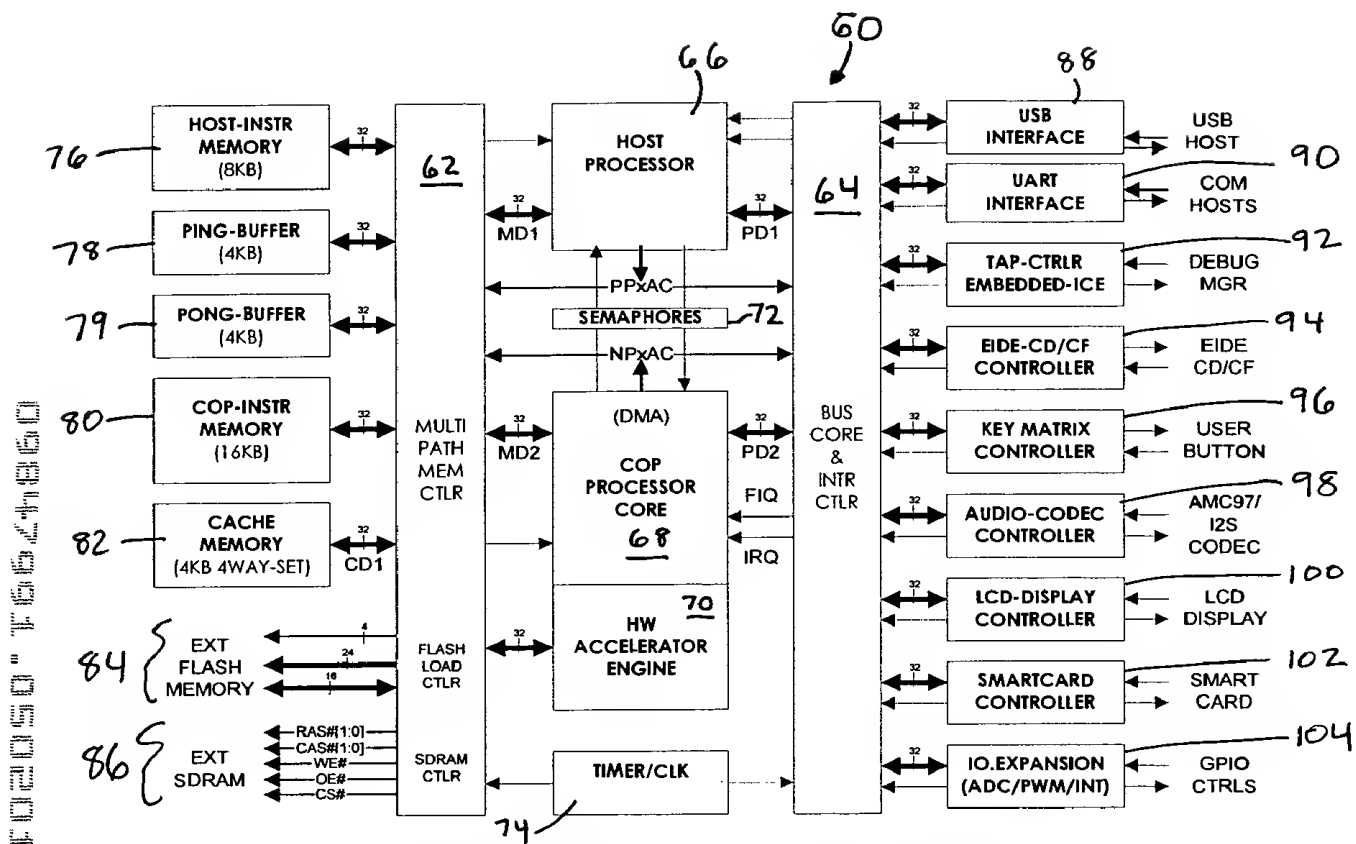


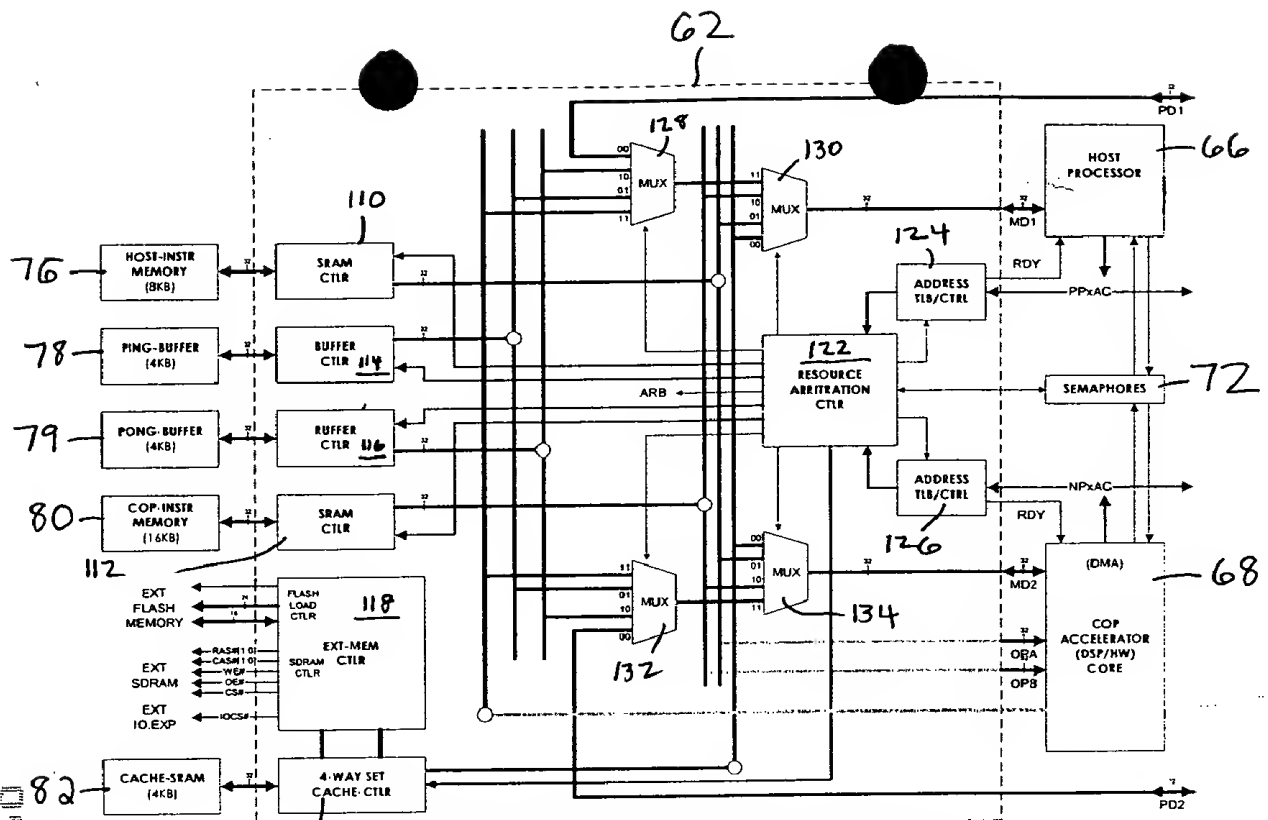
Figure 5E

T02050-1664860



Functional Block Diagram of SDMI-SOC

Figure 6



Functional Block Diagram of Processor Bus Architecture

Figure 7

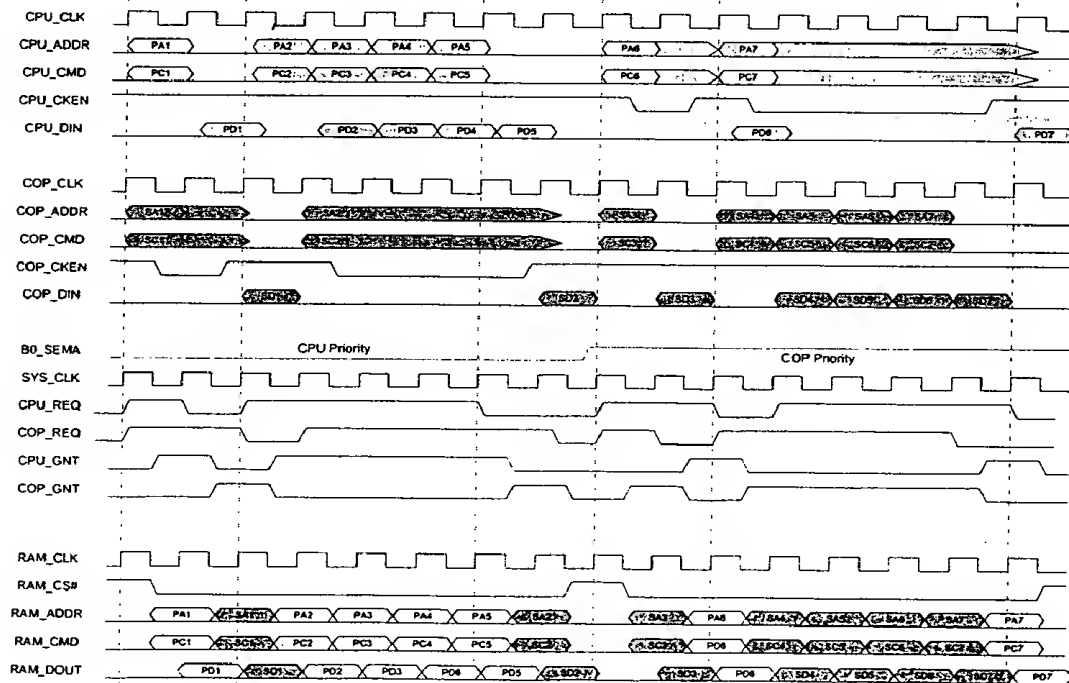


Figure 8

094791.05031

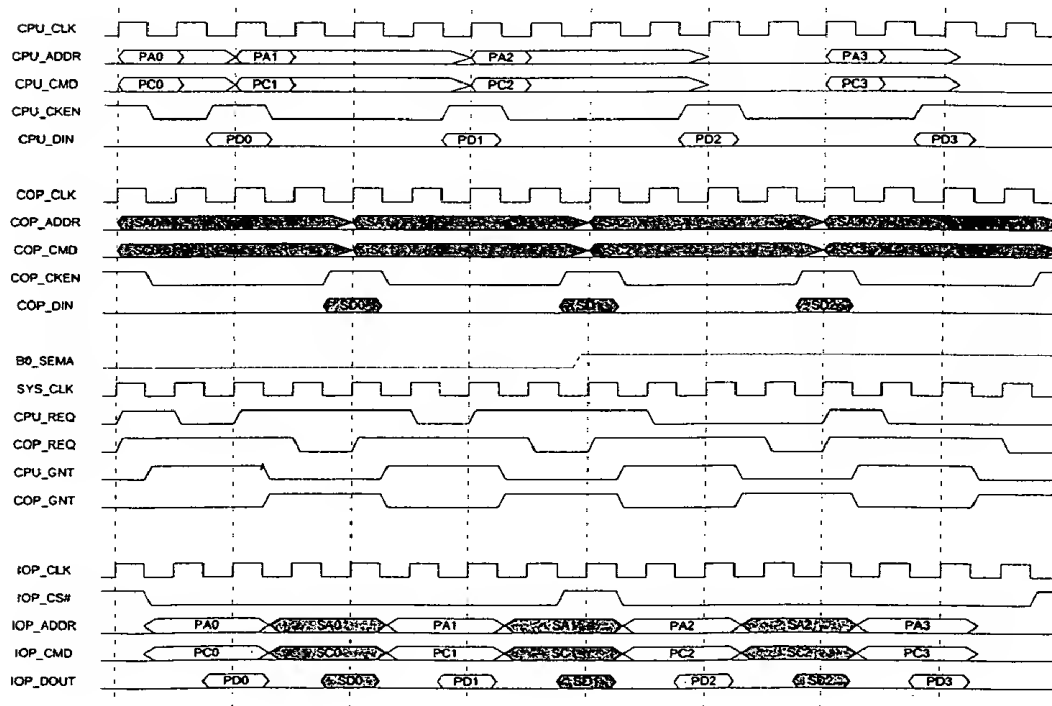
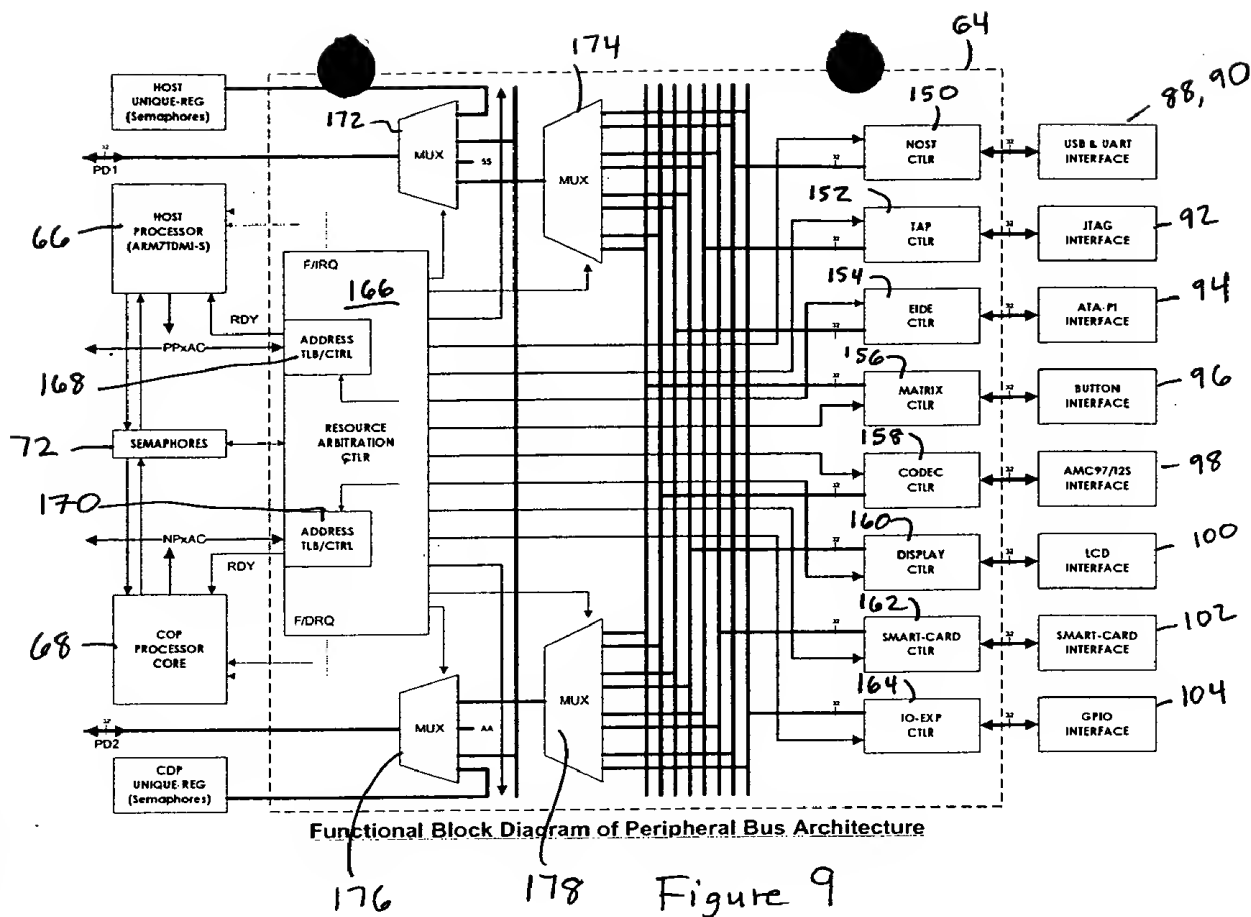


Figure 10